

**ABSTRACT OF THE DISCLOSURE**

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2       A multi-service integrated hardware circuit (20) transmits cells  
3 between an external interface (62) and plural on-board service devices  
4 (70<sub>1</sub>, 70<sub>2</sub>, 72, and 74) handling differing telecommunication services. The  
5 on-board service devices include one or more ATMF transceivers (70<sub>1</sub>,  
6 70<sub>2</sub>), a Utopia 2 level device (74), and a circuit emulator (72) which  
7 interfaces with one of a PCM interface (30<sub>1</sub>, 30<sub>2</sub>) and an E1 or TI interface  
8 (32). The multi-service circuit comprises a multiplexer/demultiplexer core  
9 (60) which connects to the external interface (62) and which connects via  
10 an internal interface (64) to the plural service devices. In the illustrated  
11 embodiments, the external interface (62) and internal interface (64) are  
12 Utopia level 2 interfaces.